

Substitute for form 1449A/PTO

Complete if Known

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Application Number** 10/607,769**Filing Date** June 27, 2003**First Named Inventor** Hareland, Scott A.**Group Art Unit** 2891**Examiner Name** Such, Matthew W.

Sheet 1 of 7

**Attorney Docket No:** 42P15685**US PATENT DOCUMENTS**

Examiner Initial *	Cite No. <sup>1</sup>	USP Document Number	Publication or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	<b>First Named Inventor</b>	Hareland, Scott A.
	<b>Group Art Unit</b>	2891
	<b>Examiner Name</b>	Such, Matthew W.
Sheet 2 of 7		Attorney Docket No: 42P15685

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Sheet 3 of 7	Attorney Docket No: 42P15685	

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Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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 \* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /MWS/

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>	<i>Complete if Known</i>	
	<b>Application Number</b>	10/607,769
	<b>Filing Date</b>	June 27, 2003
	<b>First Named Inventor</b>	Hareland, Scott A.
	<b>Group Art Unit</b>	2891
	<b>Examiner Name</b>	Such, Matthew W.
Sheet 7 of 7	Attorney Docket No: 42P15685	

/MWS/		TOKORO, KENJI , et al., "Anisotropic Etching Properties of Silicon in KOH and TMAH Solutions", International Symposium on Micromechatronics and Human Science, IEEE, (1998), pp. 65-70	
/MWS/		WOLF, STANLEY , et al., "Wet Etching Silicon", Silicon Processing for the VLSI Era, Vol. 1: Process Technology, Lattice Press, Sunset Beach, CA, (Sept. 1986), 3 pages	

EXAMINER

/Matthew Such/

DATE CONSIDERED

07/20/2008

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